IN THE CLAIMS

1-47.   (Canceled)

48.   (Currently Amended) A data processing system, comprising:

means for storing an obfuscated stream, the obfuscated stream comprising parts which
are interleaved, the parts having been taken from at least two operative
instruction streams including a first operative instruction stream and a second
operative instruction stream, the first operative instruction streams being
compiled from a first source code, the second operative instruction streams
being compiled from a second source code separate from the first source code;
and

means for executing the obfuscated stream;

wherein the parts include a second part ~~of the second operative instruction stream is~~
interleaved between a first part and a third part, the second part being of the
second operative instruction stream, the first part and the third part being of
the first operative instruction stream; ~~and~~

wherein the second part is reachable from the first part during the execution; and

wherein when the first part and the third part are executed, the second part is also
executed.

49.   (Previously Presented) The data processing system of claim 48, wherein the second
part is stack balanced.

50. (Previously Presented) The data processing system of claim 48, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

51. (Previously Presented) The data processing system of claim 48, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

52. (Previously Presented) The data processing system of claim 48, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

53. (Previously Presented) The data processing system of claim 48, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

54. (Previously Presented) The data processing system of claim 48, wherein two of the operative instructions streams are the same.

55. (Currently Amended) A digital processing system, comprising:
     memory to store an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams including a first operative instruction stream and a second

operative instruction stream, the first operative instruction streams being

compiled from a first source code, the second operative instruction streams

being compiled from a second source code separate from the first source code;

and

a processor coupled with the memory, the processor to execute the obfuscated stream;

wherein the parts includes a second part of the second operative instruction stream is

interleaved between a first part and a third part, the second part being of the

second operative instruction stream, the first part and the third part being of

the first operative instruction stream; and

wherein the second part is reachable from the first part during the execution; and

wherein when the first part and the third part are executed, the second part is also

executed.

56.   (Previously Presented) The digital processing system of claim 55, wherein the second

part is stack balanced.

57.   (Previously Presented) The digital processing system of claim 55, wherein the

memory comprises DRAM (Dynamic Random Access Memory); and wherein the

obfuscated stream is stored temporarily in the DRAM.

58.   (Previously Presented) The digital processing system of claim 55, wherein the

obfuscated stream further comprises an obfuscation code that interrelates the parts

from the operative instruction streams.

59.   (Previously Presented) The digital processing system of claim 58, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

60.   (Previously Presented) The digital processing system of claim 55, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

61.   (Previously Presented) The digital processing system of claim 60, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

62.   (Previously Presented) The digital processing system of claim 55, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

63.   (Previously Presented) The digital processing system of claim 55, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

64.   (Previously Presented) The digital processing system of claim 63, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

65.	(Previously Presented) The digital processing system of claim 55, wherein two of the operative instructions streams are the same.

66.	(Previously Presented) The digital processing system of claim 65, wherein the memory comprises DRAM (Dynamic Random Access Memory); and wherein the obfuscated stream is stored temporarily in the DRAM.

67.	(Currently Amended) A server data processing system, comprising:

means for storing an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams including a first operative instruction stream and a second operative instruction stream, the first operative instruction streams being compiled from a first source code, the second operative instruction streams being compiled from a second source code separate from the first source code;

means for transferring the obfuscated stream to a client data processing system through a network for execution;

wherein the parts include a second part of the second operative instruction stream is interleaved between a first part and a third part, the second part being of the second operative instruction stream, the first part and the third part being of the first operative instruction stream; and

wherein the second part is reachable from the first part during the execution; and

wherein when the first part and the third part are executed, the second part is also executed.

68.   (Previously Presented) The server processing system of claim 67, wherein the second part is stack balanced.

69.   (Previously Presented) The server processing system of claim 67, wherein the obfuscated stream further comprises an obfuscation code that interrelates the parts from the operative instruction streams.

70.   (Previously Presented) The server processing system of claim 67, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

71.   (Previously Presented) The server processing system of claim 67, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

72.   (Previously Presented) The server processing system of claim 67, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

73.   (Previously Presented) The server processing system of claim 67, wherein two of the operative instructions streams are the same.

74.   (Currently Amended) A server digital processing system, comprising:

memory to store an obfuscated stream, the obfuscated stream comprising parts which

are interleaved, the parts having been taken from at least two operative

instruction streams including a first operative instruction stream and a second

operative instruction stream, the first operative instruction streams being

compiled from a first source code, the second operative instruction streams

being compiled from a second source code separate from the first source code;

a processor coupled with the memory; and

a communication device coupled with the processor, the communication device to

communicate the obfuscated stream to a client data processing system through

a network for execution;

wherein the parts include a second part ~~of the second operative instruction stream is~~

interleaved between a first part and a third part, the second part being of the

second operative instruction stream, the first part and the third part being of

the first operative instruction stream; ~~and~~

wherein the second part is reachable from the first part during the execution; and

wherein when the first part and the third part are executed , the second part is also

executed.

75.   (Previously Presented) The server digital processing system of claim 74, wherein the

second part is stack balanced.

76.   (Previously Presented) The server digital processing system of claim 74, wherein the

obfuscated stream further comprises an obfuscation code that interrelates the parts

from the operative instruction streams.

77.   (Previously Presented) The server digital processing system of claim 74, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

78.   (Previously Presented) The server digital processing system of claim 74, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

79.   (Previously Presented) The server digital processing system of claim 74, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

80.   (Previously Presented) The server digital processing system of claim 74, wherein two of the operative instructions streams are the same.

81.   (Previously Presented) The server digital processing system of claim 74, wherein the communication device comprises a network interface.

82.   (Previously Presented) The server digital processing system of claim 74, wherein the network interface comprises an Ethernet interface.

83.   (Currently Amended) A method, comprising:

storing an obfuscated stream, the obfuscated stream comprising parts which are

interleaved, the parts having been taken from at least two operative instruction

streams including a first operative instruction stream and a second operative

instruction stream, the first operative instruction streams being compiled from

a first source code, the second operative instruction streams being compiled

from a second source code separate from the first source code;

transferring the obfuscated stream to a client data processing system through a

network;

wherein the parts include a second part of the second operative instruction stream is

interleaved between a first part and a second part, the second part being of the

second operative instruction stream, the first part and the third part being of

the first operative instruction stream; and

wherein the second part is reachable from the first part during the execution; and

wherein when the first part and the third part are executed, the second part is also

executed.

84.   (Previously Presented) The method of claim 83, wherein the second part is stack
balanced.

85.   (Previously Presented) The method of claim 83, wherein the obfuscated stream
further comprises an obfuscation code that interrelates the parts from the operative
instruction streams.

86. (Previously Presented) The method of claim 83, wherein at least one of the parts has been transformed before the parts are interleaved and after the parts are taken from the operative instruction streams.

87. (Previously Presented) The method of claim 83, wherein at least one of the parts has been so transformed before the parts are interleaved and after the parts are taken from the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

88. (Previously Presented) The method of claim 83, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

89. (Previously Presented) The method of claim 83, wherein two of the operative instructions streams are the same.

90. (Currently Amended) A machine readable medium containing executable computer program instructions which when executed by a data processing system cause said system to perform a method, the method comprising:
storing an obfuscated stream, the obfuscated stream comprising parts which are interleaved, the parts having been taken from at least two operative instruction streams including a first operative instruction stream and a second operative instruction stream, the first operative instruction streams being compiled from

a first source code, the second operative instruction streams being compiled

from a second source code separate from the first source code;

downloading the obfuscated stream to a client data processing system through a

network;

wherein the parts include a second part ~~of the second operative instruction stream is~~

interleaved between a first part and a second part, the second part being of the

second operative instruction stream, the first part and the third part being of

the first operative instruction stream; ~~and~~

wherein the second part is reachable from the first part during the execution; and

wherein when the first part and the third part are executed, the second part is also

executed.

91.   (Previously Presented) The medium of claim 90, wherein the second part is stack

balanced.

92.   (Previously Presented) The medium of claim 90, wherein the obfuscated stream

further comprises an obfuscation code that interrelates the parts from the operative

instruction streams.

93.   (Previously Presented) The medium of claim 90, wherein at least one of the parts has

been transformed before the parts are interleaved and after the parts are taken from the

operative instruction streams.

94.   (Previously Presented) The medium of claim 90, wherein at least one of the parts has

been so transformed before the parts are interleaved and after the parts are taken from

the operative instruction streams that the obfuscated stream performs at least the same logical operations of one of the operative instruction streams.

95.   (Previously Presented) The medium of claim 90, wherein one of the operative instruction streams has been transformed before the parts are taken from the operative instruction streams.

96.   (Previously Presented) The medium of claim 90, wherein two of the operative instructions streams are the same.